



160mA Single channel LED Driver

Features

- Up to 160mA single channel constant current regulator
- 1.6V ~ 18V wide supply voltage range supports self-power structure in lighting application
- Current set by an external resistor
- Minimized 1V (160mA) dropout voltage
- Fast current rising and falling
- Less than ±5% Chip to Chip current skew
- Less than $\pm 0.5\%$ /V load (or line) regulation
- 125 °C ~160 °C junction temperature current ramp down thermal protect
- -40° C ~ 85° C ambient operating temperature
- Cascade-able for higher voltage drop applications

Product Description

NU510S is a medium power linear current regulation component that can be easily used in various LED lighting applications. It is equipped the excellent feature of good load/line regulation capability, minimized chip current skew, stable output current in high power or load voltage fluctuating environment that can be used in wide area of LED lighting source to maintain the uniformity of light intensity.

Except for the power supply function, the VDD pin of NU510S is output enable (OE), and can be used in digital PWM controlled circuits for more precise current adjustment in gray level applications.

With the feature of wide power supply range design and ultra low I_{DD} consumption, the NU510S supports the self powered structure in LED lighting applications. In this structure, the NU510S no need to be provided a dedicate power circuit even the system power voltage is much higher than the maximum operation voltage of NU510S. The V_{DD} power can be gotten from the proper position in LED series of system.

Applications

- General LED lighting
- Decoration lighting for architecture
- LED torch / flash light
- RGB lighting
- RGB display / indicator

Package Type

ESOP 8 (Part No.: NU510SES)



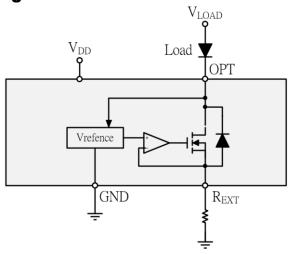
Terminal Description

Pin name	Function	
GND	Ground (Thermal pad)	
R _{EXT}	Current setting Resistor	
OPT	Current sink	
V_{DD}	Power supply	
Thermal pad	Ground potential	

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Block Diagram



Maximum Ratings (T = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{DD}	0 ~ 20	V
Output voltage(Output enable)	$V_{\mathrm{OPT_Enable}}$	-0.2 ~ 16	V
Output voltage(Output disable)	$V_{OPT_Disable}$	-0.2 ~ 20	V
Output current	I_{OPT}	200	mA
Power Dissipation (Ta=25°C)	PD	1	W
Thermal Resistance (On PCB, Ta=25°C)	$R_{TH(j-a)}$	100	°C/W
Operating temperature	T_{OPR}	-40~+85	°C
Storage temperature	T_{STG}	-55~+150	°C

Electrical Characteristics and Recommended Operating Conditions

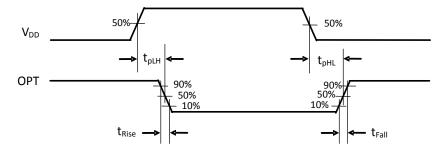
Characteristic	Symbol	Condition		Min.	Typ.	Max.	Unit
Supply voltage	$V_{ m DD}$	Room Temp. $V_{OPT} = 1V$		1.6	-	18	V
Supply voltage rising and	***	$V_{DD} \leq 5V$		0.05	-	-	G
falling speed *1	V_{DDspd}	V _{DD} :	> 5V	5	-	-	uS
Output voltage	V _{OPT_Enable}	V_{DD} > $P_{D} \leq 1$	1.6V, P _{D_recomd}	-	-	15	V
	$V_{OPT_Disable}$	V_{DD} <	0.8V	-	-	18	V
Supply current	I_{DD}	V_{DD}	≦18V	-	75	100	uA
			$I_S \leq 20 mA$	-	0.4	-	
Minimum dropout voltage	$V_{ m OUT}$	$V_{DD} \ge 3V$	$I_S \leq 80 \text{mA}$	-	0.6	-	V
			$I_S \leq 160 \text{mA}$	-	1	-	
Output current	I_{OPT}	$V_{DD} \ge 3V$		-	-	160	mA
Current set voltage	V_{REXT}	V _{DD} > 1.6V, Room Temp.		152	160	168	mV
Leakage	$I_{ m Leakage}$	$V_{\mathrm{DD}} = 0 V,$ $V_{\mathrm{OPT}} = 15 V$		-	-	0.5	uA
Line regulation	$%/V_{DD}$	$13V > V_{DD} > 3V$		-	-	±0.5	%/V
Load regulation	$%/V_{P}$	$15V > V_{OPT} > 0.4V$,		-	-	±0.5	%/V
Thermal regulation	%/10°C	$V_{DD} = 3V$, $V_{OPT} = 1V$, Temperature $< 125^{\circ}C$		-1	-	0	%/10°C
Output ramp down temperature	Т1	Output enabled		-	125	-	°C
Shutdown temperature	T2	I _{OPT} =0		-	160	-	
Chip current skew	I_{Skew}	$V_{DD} = 3V$, $V_{OPT} = 1V$		-	2	5	%
Power Dissipation	P_{D_recomd}	Room Temp.		-	-	0.65	W

^{*1} For the stable reason, the rising and falling speed of supply voltage (VDD) on NU510S should be slower when higher VDD than 5V is adopted. Fast and high VDD transition will bring the timing of output current instable. Please refer to typical application circuit in this specification for proper using.

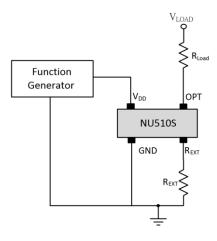
Switching Characteristics (T = 25°C)

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
Propagation Delay Time V _{DD} from "L" to "H"	$t_{ m pLH}$	$V_{OPT}=1V, V_{DD}=0V \rightarrow 3V$		0.5	1	uS
Output current rising time	t_{Rise}	$V_{OPT}=1V$, $V_{DD}=0V \rightarrow 3V$		0.8	1.5	uS
Propagation Delay Time V_{DD} from "H" to "L"	$t_{ m pHL}$	$V_{OPT}=1V, V_{DD}=3V \rightarrow 0V$		30	100	nS
Output current falling time	t_{Fall}	$V_{OPT}=1V, V_{DD}=3V \rightarrow 0V$		100	300	nS

Timing Waveform



Test Circuit



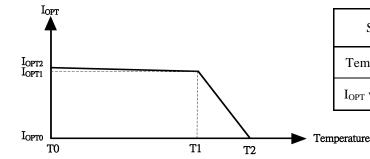
Output Current Setting

The output current of NU510S is set by an external resistor (R_{EXT}). The output current can be figured out by following equation.

$$I_{\mathit{OPT}} \cong \frac{0.16 V}{R_{\mathit{EXT}} + 0.2 \Omega}$$

Thermal protection

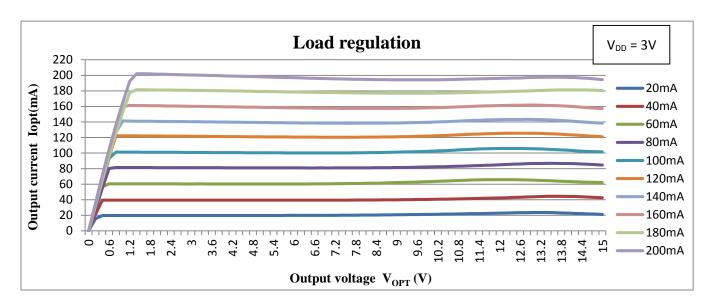
When junction temperature is more than thermal protection temperature (~125°C), the output current of NU510S will start to decrease to lower down the power dissipation on chip. If the junction temperature reach 160°C, the output current will almost shut down. The output current will restore in the same way when the temperature decrease.

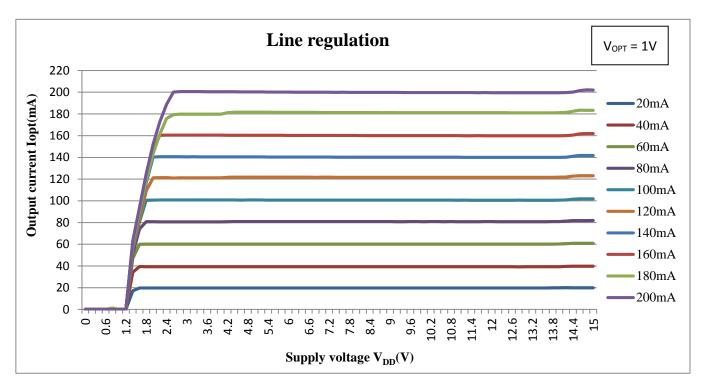


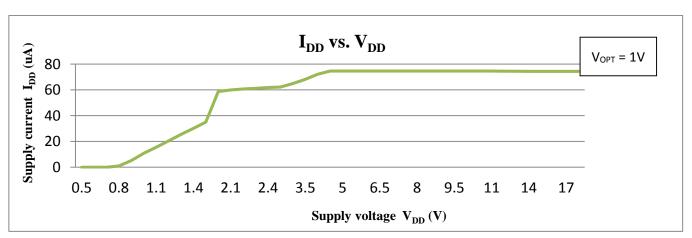
State	Normal $(T0 \leftrightarrow T1)$	Thermal protect $(T1 \leftrightarrow T2)$	Unit
Temperature	-40 ↔ 125	125 ↔ 160	°C
I _{OPT} variation	-0.8	-28	%/10°C

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Output I/V Curve

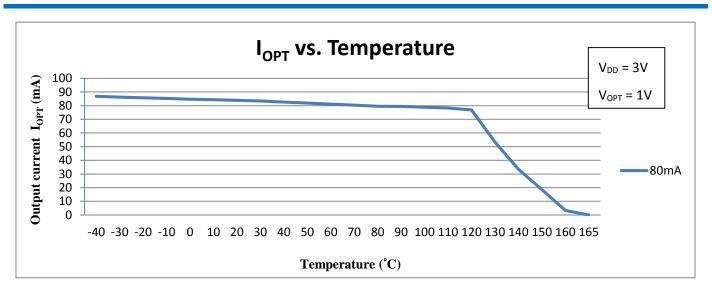






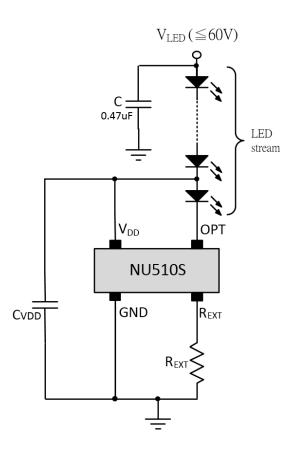
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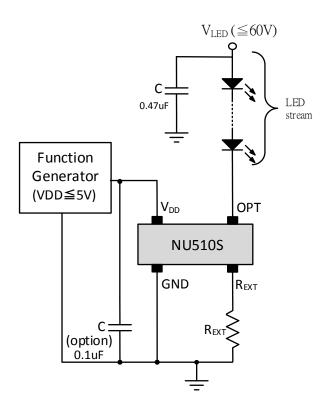


Typical Application Circuit

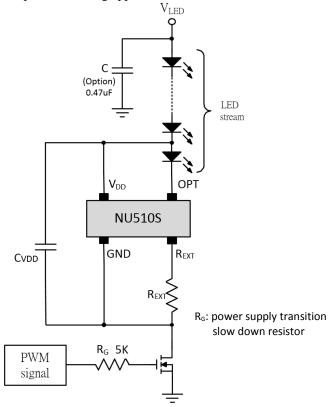
• DC power general lighting



DC PWM dimming application



DC power dimming application

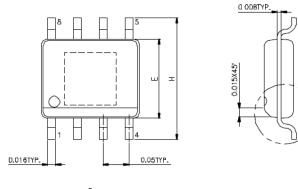


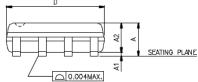
Note: Generally, The capacitance of C_{VDD} capacitor when self-power structure is used can be related with LED typical current. For example, if the typical current of LED is 160mA, the capacitance is about 160nF. The capacitance can be adjusted according to the requirement of real applications.

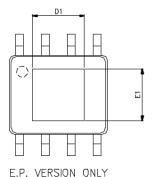
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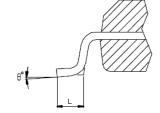
Package Dimensions

ESOP 8









SYMBOLS	MIN.	MAX.
A	0.053	0.069
A1	0.002	0.006
A2	_	0.059
D	0.189	0.196
E	0.150	0.157
Н	0.228	0.244
L	0.016	0.050
t)°	0	8

UNIT: INCH

THERMALLY ENHANCED DIMENSIONS

PAD SIZE	E1	D1
90X90E	0.081 REF	0.081 REF
95X130E	0.086 REF	0.117 REF

UNIT: INCH

Restrictions on product use

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